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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10035988	FILING DATE 12/24/2001	CLASS 710	SUBCLASS 305	GAU 2181 2165	EXAMINER <i>Rinehart Mason</i>
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**APPLICANTS: Heynemann Tom; Sprouse Jeffrey; Knowles-Michael;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	20206-29 (P00-2952)
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiner's initials			
TITLE : Method and apparatus for ensuring multi-threaded transaction ordering in a strongly ordered computer interconnect			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
			Sheets Drwg.	Figs. Drwg.
TERMINAL DISCLAIMER		Application Examiner		
PREPARED FOR ISSUE				
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